

<b>Notice of References Cited</b>	Application/Control No. 10/619,884		Applicant(s)/Patent Under Reexamination MALDEI ET AL.	
	Examiner Brook Kebede		Art Unit 2823	Page 1 of 1

**U.S. PATENT DOCUMENTS**

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
X	A	US-4,956,313	09-1990	Cote et al.	438/672
X	B	US-5,814,862	09-1998	Sung et al.	257/344
X	C	US-5,858,837	01-1999	Sakoh et al.	438/255
X	D	US-6,159,808	12-2000	Chuang, Shu-Ya	438/291
X	E	US-6,236,079	05-2001	Nitayama et al.	257/306
X	F	US-6,319,840	11-2001	Costrini et al.	438/706
X	G	US-6,383,868	05-2002	Parekh et al.	438/253
X	H	US-6,690,050	02-2004	Taniguchi et al.	257/296
	I	US-			
	J	US-			
	K	US-			
	L	US-			
	M	US-			

**FOREIGN PATENT DOCUMENTS**

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
	O					
	P					
	Q					
	R					
	S					
	T					

**NON-PATENT DOCUMENTS**

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Wolf et al., Silicon Processing for The VLSI Era, Volume 1: Process Technology, Pp. 184, (1986).
	V	
	W	
	X	

\*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)  
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.